



Enabling Molecular Advances in Microelectronics & Optoelectronics

**Organosilicon
and
Metal-Organic Precursors**



**Meeting the design challenges posed
by systems and components for:**

- *Metallization*
- *Dielectrics*
- *Lithography*
- *Encapsulation & Die Attach Adhesives*
- *OLEDs, LEDs, PLEDs, Phosphorescents, Electrochromics*



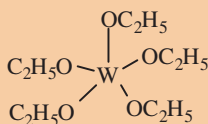
Microelectronics & Optoelectronics

Microelectronics

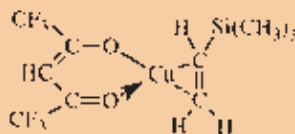


METALLIZATION

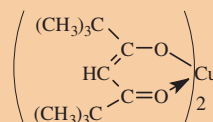
Gelest offers precursor materials for metallization applications derived from Group III & IV elements (Si, Al, Ti, Ta, W, In, Sb, Ge) used to create conductive coatings on silicon, germanium, silicon carbide, sapphire and plastic substrates. These precursors are suitable for various deposition techniques such as ALD, CVD, MOCVD and PECVD.



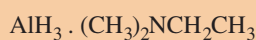
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AKC252.8



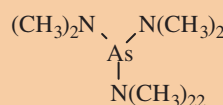
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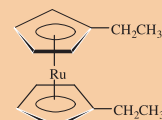
OMAL005



OMAL008



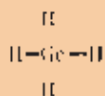
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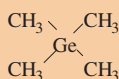
OMRU027

Strained Silicon – Silicon sources such as monochlorosilane, dichlorosilane, 1MS, 2MS, 3MS, 4MS and Germanium sources such as germane, t-butylgermane, germanium tetrachloride and other analogs are used in production of strained silicon using metal organic vapor phase epitaxy (MOVPE) to improve chip performance and lower energy consumption.

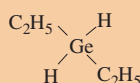
SiGe Precursors



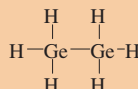
GEG5001



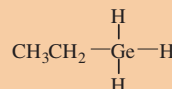
GET7550



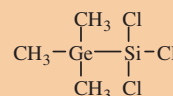
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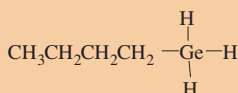
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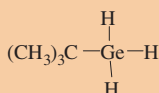
GEE4695



GET8561



GEB1969.5



GEB1970



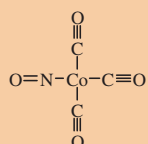
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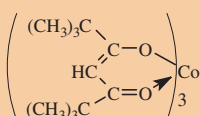
GEG5350

Barrier Layers – Group IV material are used to reduce electromigration and other effects that Cu and Al have on Si and SiO₂ insulator properties and adhesion, while reducing metal corrosion. Typical deposition methods include PVD, CVD and MOCVD.

Cobalt

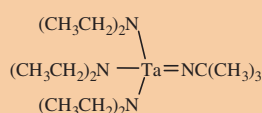


INC0032

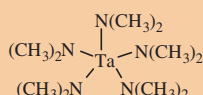


AKC240

Tantalum



OMTA082

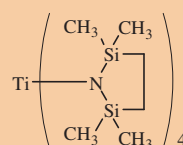


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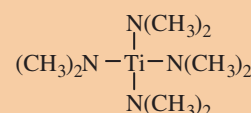


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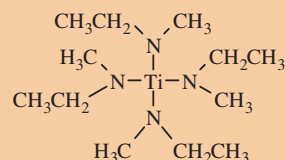
Titanium



SIT8008.0



OMTI088



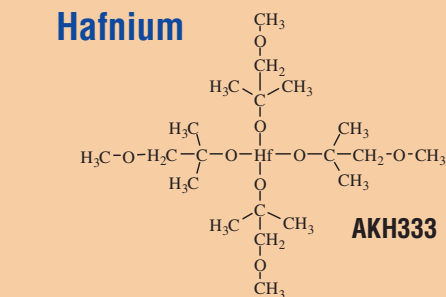
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DIELECTRICS

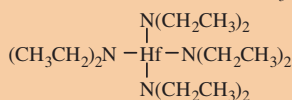
Gelest has developed patented "chloride-free" chemical process technology to commercially produce Group IV materials for use as gate dielectrics and ILD (inter-layer dielectrics). Typical Group IV materials for gate dielectrics are compounds of Hf, Zr, and rare earths such as Ce, La, Pr. Typical ILD precursors are Si based. In addition, Si based materials will play a critical role in future generations of porous dielectric materials that will require improved adhesion, mechanical and thermal properties. Porous ULK dielectrics will require the use of CAPS.

Gate Dielectrics (High-K)

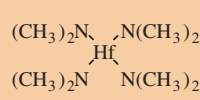
Hafnium



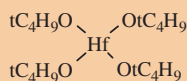
AKH333



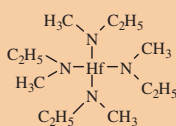
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OMHF080

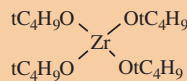


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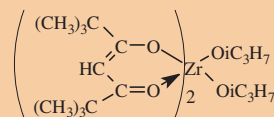


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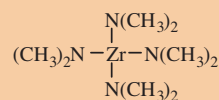
Zirconium



AKZ946



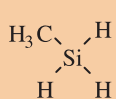
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TDMAZ OMZR080

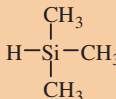
Inter-Layer Dielectrics (Low-K)

Thin Film



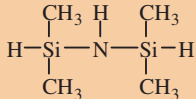
1MS

SIM6515.0



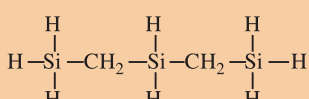
3MS

SIT8570.0



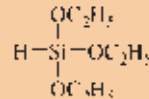
TMDZ

SIT7542.0



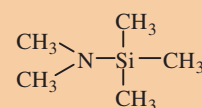
TSP

SIT8709.8

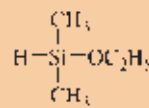


SIT8185.0

Pore Sealing & CAPS

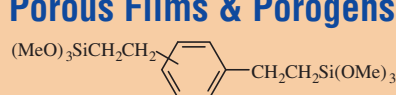


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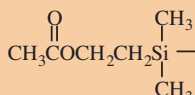


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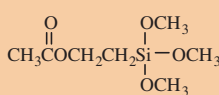
Porous Films & Porogens



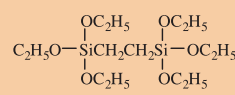
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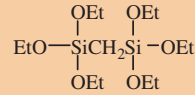
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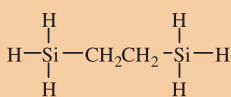


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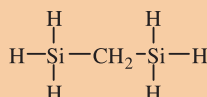


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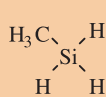
Etch-Stop Layers



SID4593.0



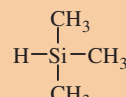
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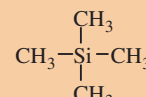
1MS SIM6515.0



2MS SID4230.0

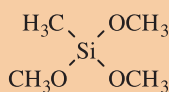


3MS SIT8570.0

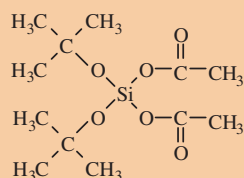


4MS SIT7555.0

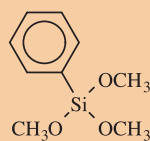
SiO₂ Source



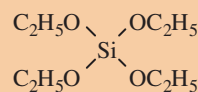
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DABS SID2790.0



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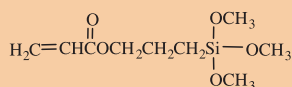


TEOS SIT7110.2

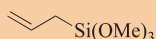
MEMS, NEMS, SAMs

SAMs (Self-Assembled Monolayers) – Group IV materials can be applied neat or in solution via conventional lithography techniques to form SAMs. SAM is a layer of amphiphilic molecules created by the chemisorption onto a metal oxide, precious metal surface, plastic or nanoparticle substrates, followed by the 2-dimensional alignment of hydrophobic groups to form a structures single monolayer. The surface can be selectively modified to achieve the desired anti-sticktion, mechanical and chemical properties for microelectromechanical systems (MEMS) and nanoelectromechanical systems (NEMS).

Metal Oxides



SIA0200.0



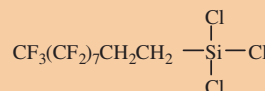
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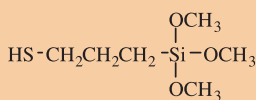
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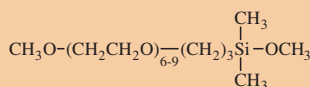
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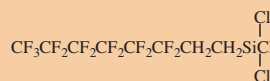
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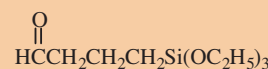
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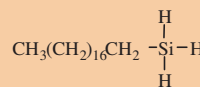


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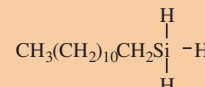
Gold, Silicon, Titanium Surfaces



SIT8173.0



SIO6635.0



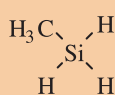
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EPITAXY

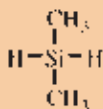
Volatile Carbosilanes and Higher Polysilanes – *the preeminent precursors for:*

- Amorphous Silicon
- Silicon Carbide films and buffer layers
- Carbon-doped (Tensile-Strained) Silicon
- SiCO:H films for low-k, barrier layers and etch-stop
- ALD promoted patterning and seed layers
- Silicon Carbonitride passivation

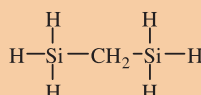
Carbosilanes



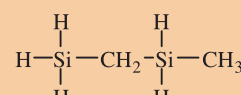
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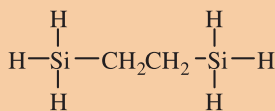
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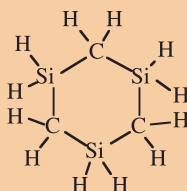
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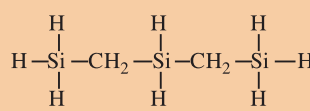
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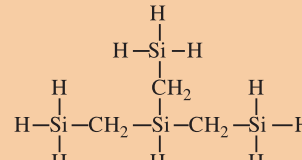
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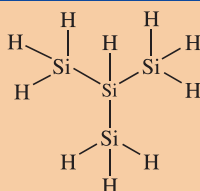


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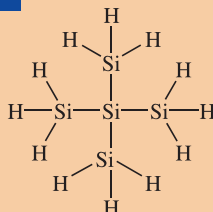


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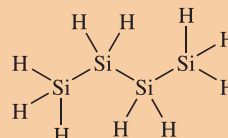
Volatile Higher Silanes



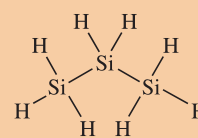
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SIN6597.07



SIT7880.0



SIT8709.6



Gelest, Inc.

Provides technical expertise in silicon and metal-organic materials for applications in Microelectronics & Optoelectronics. The core manufacturing technology of Gelest is silanes, silicones and metal-organics with the capability to handle flammable, corrosive and air sensitive liquids, gases and solids. Headquartered in Morrisville, PA, Gelest is recognized worldwide as an innovator, manufacturer and supplier of commercial and research quantities serving advanced technology markets through a materials science driven approach. The company provides focused technical development and application support for: semiconductors, optical materials, pharmaceutical synthesis, diagnostics and separation science, and specialty polymeric materials.

For additional information on Gelest's Silicon and Metal-Organic based products or to inquire how we may assist in ***Enabling Your Technology***, please contact:



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